	Feature	ISE WebPACK™	ISE BaseX	ISE Foundation	ISE Alliance
Devices	Virtex™ Series Including the new Virtex-4 Family	**Virtex-E™:** V50E -V300E **Virtex-II™:** 2V40 - 2V250 **Virtex-II Pro™:** 2VP2	**Virtex:** V50 - V600 **Virtex-E:** V50E - V600E **Virtex-II:** 2V40 - 2V500 **Virtex-II Pro:** 2VP2, 2VP4, 2VP7	ALL	ALL
	Spartan™ Series	**SpartanII/IIE:** ALL (except XC2S400E and XC2S600E) **Spartan-3™:** 3S50, 3S200, 3S400	**Spartan-II/IIE:** ALL **Spartan-3:** 3S50, 3S200, 3S400	**Spartan-II/IIE:** ALL **Spartan-3:** ALL	**Spartan-II/IIE:** ALL **Spartan-3:** ALL
	CoolRunner™ XPLA3 CoolRunner-II CoolRunner-IIA	ALL	ALL	ALL	ALL
	XC9500 Series	ALL	ALL	ALL	ALL
Design Entry	Schematic Editor	Yes	MS Windows and Linux only	MS Windows and Linux only	No
	HDL Editor	Yes	Yes	Yes	Yes
	State Diagram Editor	Yes	MS Windows only	MS Windows only	MS Windows only
	CORE Generator System	No	Yes	Yes	Yes
	PACE (Pinout and Area Constraint Editor)	Yes	Yes	Yes	Yes
	Architecture Wizards Clocking Wizard RocketIO™ Wizard ChipSync™ Wizard (Virtex-4 Only) XtremeDSP™ Slice Wizard (Virtex-4 Only)	Yes	Yes	Yes	Yes
	Third Party RTL Checker Support	Yes	Yes	Yes	Yes
	Xilinx System Generator for DSP	No	Sold as an Option	Sold as an Option	Sold as an Option
Embedded System Design	Embedded Design Kit (EDK)	No	Sold as an Option	Sold as an Option	Sold as an Option
Synthesis	Xilinx Synthesis Technology (XST)	Yes	Yes	Yes	No
	Synplicity Synplify/Pro	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
	Synplicity Amplify Physical Synthesis Support	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
	Synopsys FPGA Compiler II	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
	Synopsis DC-FPGA Compiler	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
	Mentor Graphics LeonardoSpectrum	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
	Mentor Graphics Precision® RTL	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
	Mentor Graphics Precision® Physical	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
	ABEL	CPLD	CPLD (MS Windows only)	CPLD (MS Windows only)	CPLD (MS Windows only)

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Implementation	FloorPlanner	Yes	Yes	Yes	Yes
	PlanAhead™ Hierarchical Floorplanner	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
	Constraints Editor	Yes	Yes	Yes	Yes
	Timing Driven Place and Route	Yes	Yes	Yes	Yes
	Modular Design	No	Yes	Yes	Yes
	Incremental Design	Yes	Yes	Yes	Yes
	Timing Improvement Wizard	Yes	Yes	Yes	Yes
Programming	iMPACT	Yes	Yes	Yes	Yes
	System ACE™ Configuration Manager	Yes	Yes	Yes	Yes
Board Level Integration	IBIS Models	Yes	Yes	Yes	Yes
	STAMP Models	Yes	Yes	Yes	Yes
	HSPICE Models*	Yes	Yes	Yes	Yes
Verification	ChipScope™ Pro	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
	HDL Bencher™	Yes	MS Windows only	MS Windows only	MS Windows only
	ModelSim® Xilinx Edition (MXE II)	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**
	Static Timing Analyzer	Yes	Yes	Yes	Yes
	FPGA Editor with Probe	No	Yes	Yes	Yes
	ChipViewer	Yes	Yes	Yes	Yes
	XPower (Power Analysis)	Yes	Yes	Yes	Yes
	Third Party Equivalency Checking Support	Yes	Yes	Yes	Yes
	SMARTModels for PowerPC™ and RocketIO™	No	Yes	Yes	Yes
	Third Party Simulator Support	Yes	Yes	Yes	Yes
Platforms		PC (MS Windows 2000/MS Windows XP)	PC (MS Windows 2000/MS Windows XP), Linux	PC (MS Windows 2000/MS Windows XP), Sun Solaris, Linux	PC (MS Windows 2000/MS Windows XP), Sun Solaris, Linux
IP/CORE	For more information on the complete list of Xilinx IP products, visit the Xilinx IP Center at http://www.xilinx.com/ipcenter				

*HSPICE Models available at the Xilinx Design Tools Center at *www.xilinx.com/ise*
**MXE II supports the simulation of designs up to 1 million system gates and is sold as an option.

For more information, visit the Xilinx Design Tools Center at *www.xilinx.com/ise*